

AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application.

1. (previously presented) A cable modem tuner comprising:
 - an upstream circuit for transmitting a balanced data signal to a CATV (cable television) station, wherein
 - said upstream circuit includes
 - a gain controllable gain control circuit receiving said balanced data signal after its band has been limited by a balanced type bandpass filter,
 - at least two power amplifying circuits power-amplifying the balanced data signal having been gain controlled by said gain control circuit, and
 - a control circuit transmitting a control signal to said at least two power amplifying circuits for controlling transmission/interruption of said balanced data signal.

Claims 2-6 (Canceled)

7. (currently amended) A cable modem tuner including an upstream circuit for transmitting a balanced data signal to a CATV (Cable Television) station and a receiving unit for receiving a down signal from said CATV station, comprising:
 - a duplexer for branching ~~the~~ an unbalanced data signal to said CATV station and the down signal from said CATV station; and
 - a return pass circuit outputting said balanced data signal via a converter to said duplexer;
 - said receiving unit receiving the down signal branched by said duplexer, wherein
 - said receiving unit includes
 - an up converter for converting said down signal to a first intermediate frequency signal of higher frequency,

a SAW filter for selecting the first intermediate frequency signal output from said up converter, and

a down converter converting the first intermediate frequency signal selected by said SAW filter to a second intermediate frequency signal of lower frequency for output, and

said SAW filter is formed of an oscillation circuit including a print coil or an air core coil, and

said upstream circuit includes

a gain controllable gain control circuit receiving said balanced data signal after its band has been limited by a balanced type bandpass filter,

at least two power amplifying circuits power amplifying the balanced data signal having been gain controlled by said control circuit, and

a control circuit transmitting a control signal to said at least two power amplifying circuits for controlling transmission/interruption of said balanced data signal.